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Applicant: Lukas P.P.P. van Ginneken
Title: Timing Closure Methodology
Application No.: 10/828,547 Filing Date: April 19, 2004
Examiner: Siek, Vuthe Group Art Unit: 2825
Docket No.: MDAI.001US3 Conf. No.: 3884
(formerly 054355-0309380)

Certificate of Transmission Under 37 CFR 1.8

I hereby certify that this correspondence is being facsimile transmitted to the United
States Patent and Trademark Office on June 16, 2005

Mary E. Buggin
Signature

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CORRECTION TO PATENT APPLICATION PUBLICATION

Sir:

Please make the corrections below to Patent Application Publication No. US
2005/0120319 A1, Publication Date June 2, 2005.

Page 1, section (76), Correspondence Address:

Please delete: SYNOPSYS, INC. C/O HAYNES BEFFEL & WOLFELD LLP
P.O. BOX 366
HALF MOON BAY, CA 94019 (US)

Please insert: PARSONS HSUE & DE RUNTZ LLP
655 MONTGOMERY STREET
SUITE 1800
SAN FRANCISCO, CA 94111

In support of this Request, attached is a copy of the Notice of Acceptance of Power of

Attorney Docket No.: MDAI.001US3

Application No.: 10/828,547

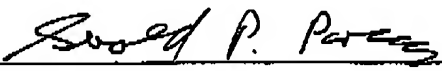
- 1 -

Attorney mailed May 13, 2005 and the Notice of Publication of Application, both showing the correspondence address that of Parsons Hsue & de Runtz LLP.

Also enclosed is a marked-up copy of page 1 of the Patent Application Publication illustrating the corrections.

The errors sought to be corrected were made by the United States Patent and Trademark Office. Therefore, no fee is believed to be due. The Commissioner is authorized, however, to charge any fee that may be required, or to credit any overpayment, against Deposit Account No. 502664. Please direct all inquiries concerning this request to the undersigned attorney at (415) 318-1160. This form is being submitted in duplicate.

Respectfully submitted,


Gerald P. Parsons
Reg. No. 24,486

June 16, 2005
Date

PARSONS HSUE & DE RUNTZ LLP
655 Montgomery Street, Suite 1800
San Francisco, CA 94111
(415) 318-1160 (main)
(415) 318-1163 (direct)
(415) 693-0194 (fax)

Attorney Docket No.: MDAI.001US3

Application No.: 10/828,547

- 2 -

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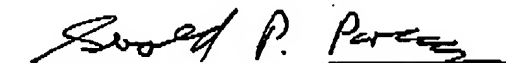
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PARSONS HSUE & DE RUNTZ LLP

655 Montgomery Street, Suite 1800 San Francisco, Ca 94111
tel 415.318.1160 fax 415.693.0194

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To United States Patent and Trademark Office
Mail Stop PGPUB
Fax number (703) 872-9306
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From Gerald P. Parsons, Reg. No. 24,486
Total pages 8
Subject Application No.: 10/828,547
Your Ref. Conf. No.: 3884
Our Ref. MDAL001US3

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Atty Docket No.: MDAL001US3

ENCLOSED:

1. Request for Correction to Patent Application Publication (2 pages - in duplicate)
2. Copy of Notice of Acceptance of Power of Attorney (1 page)
3. Copy of Notice of Publication of Application (1 page)
4. Marked-up copy of 1st page of Publication (1 page)

GPP/meb

Due Date: August 2, 2005
Transmission Date: June 16, 2005

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H241.0011453

APPLICATION NUMBER	FILING OR 371 (c) DATE	FIRST NAMED APPLICANT	ATTY. DOCKET NO./TITLE
10/828,547	04/19/2004	Lukas P.P.P. van Ginneken	054355-0309380

36257
 PARSONS HSUE & DE RUNTZ LLP
 655 MONTGOMERY STREET
 SUITE 1800
 SAN FRANCISCO, CA 94111

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MAY 19 2005

Parsons Hsue & de Runtz LLP

CONFIRMATION NO. 3884

OC000000016024147

OC000000016024147

Date Mailed: 05/13/2005

NOTICE OF ACCEPTANCE OF POWER OF ATTORNEY

This is in response to the Power of Attorney filed 05/02/2005.

The Power of Attorney in this application is accepted. Correspondence in this application will be mailed to the above address as provided by 37 CFR 1.33.

D. King
 DORIS M KING
 2800 (571) 272-1593

ATTORNEY/APPLICANT COPY



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MDA1-001453

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PARSONS HSUE & DE RUNTZ LLP
655 MONTGOMERY STREET
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SAN FRANCISCO, CA 94111

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JUN 16 2005

Parsons Hsue & de Runtz LLP

CONFIRMATION NO. 3884



OC000000016128744

Title: Timing closure methodology

Publication No. US-2005-0120319-A1
Publication Date: 06/02/2005

NOTICE OF PUBLICATION OF APPLICATION

The above-identified application will be electronically published as a patent application publication pursuant to 37 CFR 1.211, et seq. The patent application publication number and publication date are set forth above.

The publication may be accessed through the USPTO's publicly available Searchable Databases via the Internet at www.uspto.gov. The direct link to access the publication is currently <http://www.uspto.gov/patft/>.

The publication process established by the Office does not provide for mailing a copy of the publication to applicant. A copy of the publication may be obtained from the Office upon payment of the appropriate fee set forth in 37 CFR 1.19(a)(1). Orders for copies of patent application publications are handled by the USPTO's Office of Public Records. The Office of Public Records can be reached by telephone at 703-605-4283 or (800) 972-6382, by facsimile at (703) 305-8759, by mail addressed to the United States Patent and Trademark Office, Office of Public Records, Alexandria, VA 22313-1450 or via the Internet.

In addition, information on the status of the application, including the mailing date of Office actions and the dates of receipt of correspondence filed in the Office, may also be accessed via the Internet through the Patent Electronic Business Center at www.uspto.gov using the public side of the Patent Application Information and Retrieval (PAIR) system. The direct link to access this status information is currently <http://pair.uspto.gov/>. Prior to publication, such status information is confidential and may only be obtained by applicant using the private side of PAIR.

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ATTORNEY: ISH/ISH PHDRACTION: SC DATE: 6/17/05ACTION: Amend Application DATE: 6/2/05DOCKETED BY: 24 DATE: 6/16/05

VERIFICATION BY: _____ DATE: _____



US 20050120319A1

(19) **United States**(12) **Patent Application Publication**
van Ginneken(10) **Pub. No.: US 2005/0120319 A1**(43) **Pub. Date: Jun. 2, 2005**(54) **TIMING CLOSURE METHODOLOGY**

(60) Provisional application No. 60/068,827, filed on Dec. 24, 1997.

(76) **Inventor: Lukas P.P. van Ginneken, San Jose, CA (US)****Publication Classification**(51) **Int. Cl.⁷** **G06F 9/45**(52) **U.S. CL** **716/6; 716/5; 716/4**

Correspondence Address:

~~SYNOPSIS, INC. C/O HAYNES BEFFEL &~~~~WOLFELD LLP~~~~PO BOX 366~~~~HALF MOON BAY, CA 94019 (US)~~(21) **Appl. No.: 10/828,547**(22) **Filed: Apr. 19, 2004****Related U.S. Application Data**

(63) Continuation of application No. 10/134,076, filed on Apr. 24, 2002, now Pat. No. 6,725,438, which is a continuation of application No. 09/054,379, filed on Apr. 2, 1998, now Pat. No. 6,453,446.

(57) **ABSTRACT**

An automated method for designing an integrated circuit layout using a computer based upon an electronic circuit description and based upon cells which are selected from a cell library, each of the cells having an associated area, comprising the steps of: (a) placing each of the cells in the integrated circuit layout so that the cells can be coupled together by wires to form a circuit path having an associated predetermined delay constraint wherein the cells are coupled together based upon the electronic circuit description input to the computer; (b) connecting the cells together with the wires to form the circuit path; and (c) adjusting an area of at least one of the cells to satisfy the associated predetermined delay constraint of the circuit path.

PARSONS HSUE & DE RUNTZ LLP
655 MONTGOMERY STREET
SAN FRANCISCO, CA
94111

